

JPW



Docket No.: M4065.0438/P438-A
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In-re Patent Application of:
Eric T. Stubbs

Application No.: 10/660,566

Art Unit: 2818

Filed: September 12, 2003

Examiner: H. Hoang

For: HIGH SPEED MEMORY ARRAY
ARCHITECTURE

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

In response to the Office Action dated June 7, 2004, please amend the above-identified U.S. patent application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.